

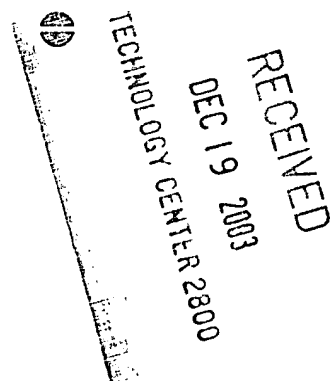
#61/21B
BOP
6-8-05

Application Number: 10/084,803
Filing Date: 02/28/2002
Applicant: Charles Douglas Murphy
Title: SHARED PARALLEL DIGITAL-TO-ANALOG CONVERSION
Examiner/GAU: Jean Bruner Jeanglaude / 2819

June 3, 2003

AMENDMENT B

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450



Sir:

In response to the Notice of Non-Compliant Amendment Letter of May 22, 2003 which was in response to Amendment A filed on May 9, 2003, please add the following material to Amendment A:

Version with Markings to Show Changes Made

In the Specification:

The Abstract on page 27 of the Application has been amended as follows

B1

~~A machine used for conversion of digital number values to analog signal values. In one embodiment, a digital-to-analog converter in which a counter tracks the elapsed time since the beginning of a conversion cycle. Simultaneously, a reference analog signal such as a voltage ramp is generated. When the count provided by the counter reaches a stored digital number value, a sample-and-hold circuit is triggered and acquires the value of the reference analog signal. A multiplicity of Multiple stored digital number values can be converted by using a multiplicity of multiple sample-and-hold and trigger circuits which share the counter and the reference analog signal. The conversion operations can share the counter and the reference analog signal. The invention~~